

# SKY-820V3 (Standard with NVMe) Quick Start Guide



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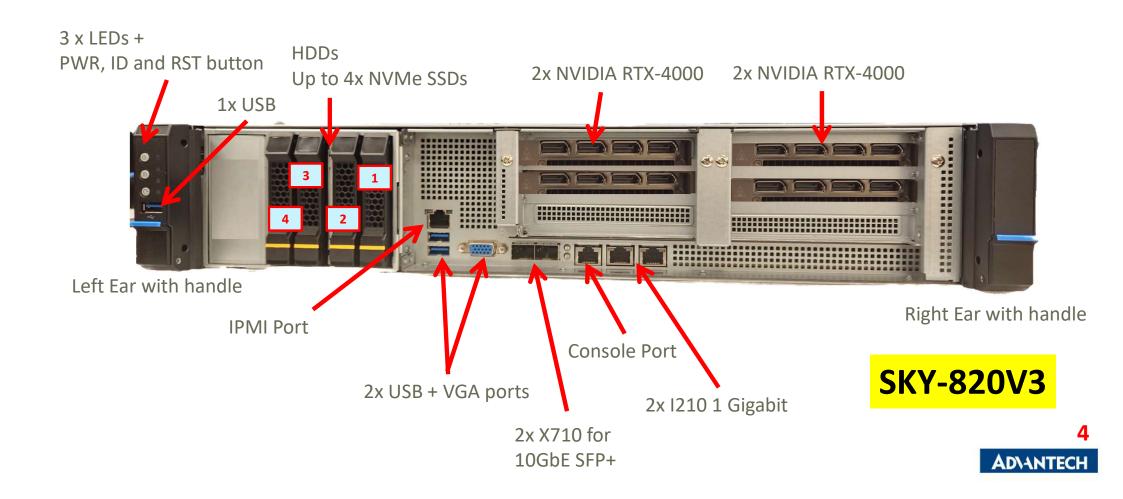
# **History**

Version	Date	Handled by	Note
Draft	2024/04/12	Sam168.Chang	
1.0	2024/04/15	Sam168.Chang	DIMM population rule
1.5	2024/04/24	Sam168.Chang	DIMM table update
2.0	2025/04/25	Sam168.Chang	Revised Standard
2.5	2025/05/25	Sam168.Chang	With GPU cards / NVMe HD

## Agenda

- System Front View & Ear Module Design Spec.
- System Rear View & PCIe Slots
- CPU location, DIMM slot ID & Population rule
- Access the BIOS via Monitor with VG port
- Operation System Installation through VGA port
- ☐ Access the Server via Console port
- BMC IP address configure from BIOS & Access via browser

### **Product Outlook – Front View**

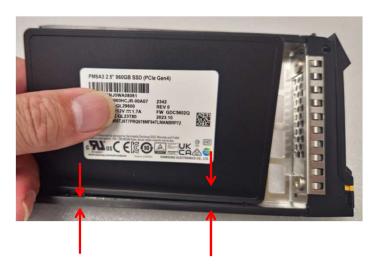


## **HDD LED**



Item	LED	Color	Description	Condition
Λ	Power LED	n/a	Stay off	Fault
А	Powel LED	Blue	Solid on	Present
		Green	Stay on	Access
A F	Activity LED		Solid on	Failure
Ь	Activity LED	Red	1Hz blink	Rebuild
			4Hz blink	Locate

## Product Outlook – Assembly 2.5" HDD [1/2]



**Step 1:** 2.5" HDD side screw hole align HDD tray emboss feature at arrow position.

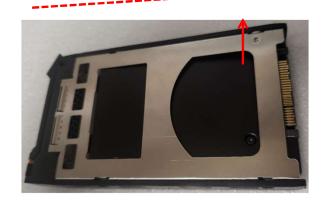


**Step 2:** Press HDD's right side.

OK



NG



**Step 3:** Need check "SLIDE-HOLD\_R" to keep flat.





## Product Outlook – Remove 2.5" HDD [2/2]



**Step 1:** Hold HDD tray at HDD tray center.



**Step 2:** Press and twist HDD tray at arrow position.



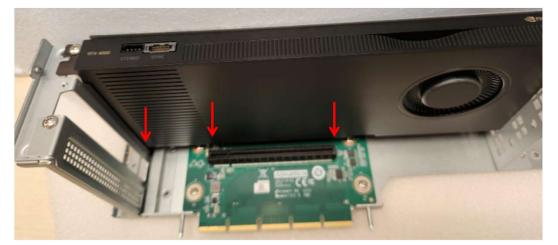
**Step 3:** Hold and Rotate from the HDD end side.



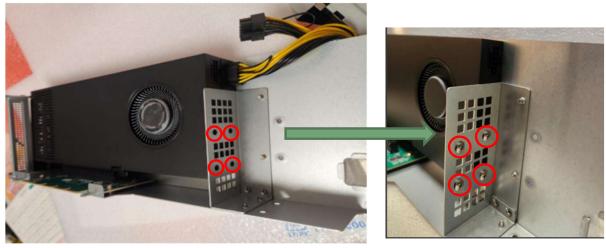
**Step 4:** Remove HDD from HDD tray.



## Product Outlook – Assembly RTX-4000 [1/2]



**Step 1:** Align the GPU card with both the PCIe slot and the bracket hole, and insert it in the direction of the arrow.



**Step 2:** Use the same procedure for the second card, then finally secure the screws at the redcircled positions.



## Product Outlook – Assembly RTX-4000 [2/2]



**Step 3:** Attach the GPU power cables to both cards. Please pay attention to the connector's orientation; do not insert it upside down.



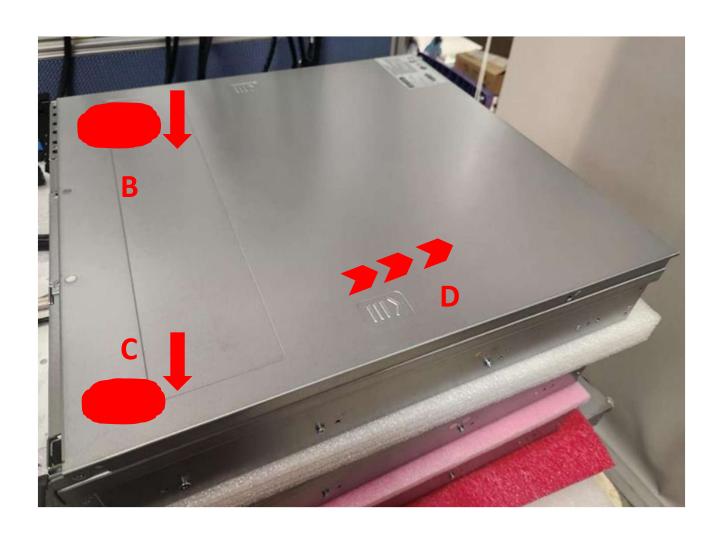
#### Remove the cards one at a time, in sequence:

- 1. Fully loosen the screws.
- 2. Unplug the cards from riser cage.
- 3. Detach each GPU power cable one by one

**Note:** Each connector has a locking latch.



## **Product Outlook – Assembly Cover**



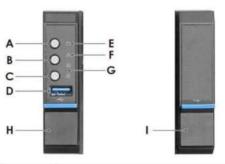
#### **Step by Step**

**Step 1:** Use plam press down cover on **B** & **C** area

**Step 2:** Follow the direction of **D** to open the cover

## **Ear Module Design Specification**

#### Left Ear Design



Label	Icon	Indicator, button or connector
A	U	Power Button
В	ID	UID Button
С	0	Reset Button
D	·~-	USB3.0
E	0	HDD Activity LED
F	Δ	System Alarm LED
G	묆	LAN1, LAN2 Activity LED
н		Rack Handle (Left)
I.		Rack Handle (Right)

LED	State	Color	Description⊲
Dawer LED. "	On	Blue	System is turned on∈
Power LED *	Off	Off	System power off←
HIDA	On	Blue	Unit being identified↩
UID↩	Off	Off	No identification request
Status I ED/I	Blinking	Red	Hardware monitor fail (Note 1)
Status LED←	Off	Off	No failure←
	On	Amber	When HDD is present
$HDD\;LED \mathord{\leftarrow}$	Blinking	Amber	When HDD is busy⊢
	Off	Off	When HDD is not present, or present but not busy
	On	Green	When Ethernet is link up←
LAN1 / LAN2 LED←	Blinking	Green	When networking is active ←
LLD	Off	Off	When Ethernet is not linked up←



### **Product Outlook – Rear View**

4x 8038 Hot-swappable Fans



2x ACBEL R1CA2801A redundant power supply

#### **SKY-820V3 PCIe Slots**

PCIe Slots ordering, top to down.

RIS1 RIS2





RISER1_PRSNT	56h	ok	11.0	Device Present
RISER1-TMP	57h	ok	11.0	29 degrees C
RIS1_PCIE1_PRSNT	58h	ok	11.0	Device Present
RIS1_PCIE2_PRSNT	59h	ok	11.0	Device Present
RISER2_PRSNT	5Ah	ok	11.1	Device Present
RISER2-TMP	5Bh	ok	11.1	29 degrees C
RIS2_PCIE1_PRSNT	5Ch	ok	11.1	Device Present
RIS2 PCIE2 PRSNT	5Dh	ok	11.1	Device Present

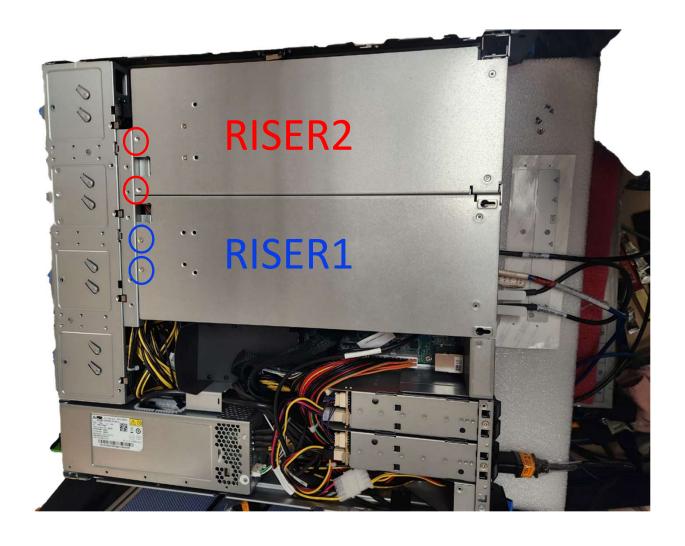


## Riser Cage Assembly [1/2]

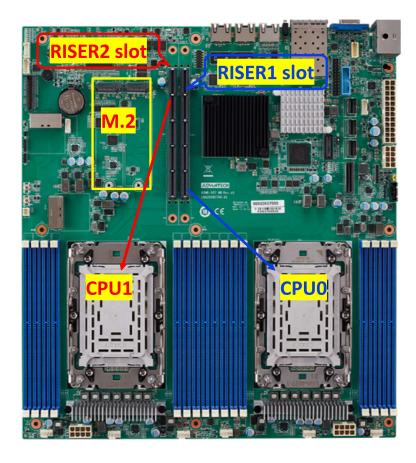
#### RISER1 RISER2



# Riser Cage Assembly [1/2]



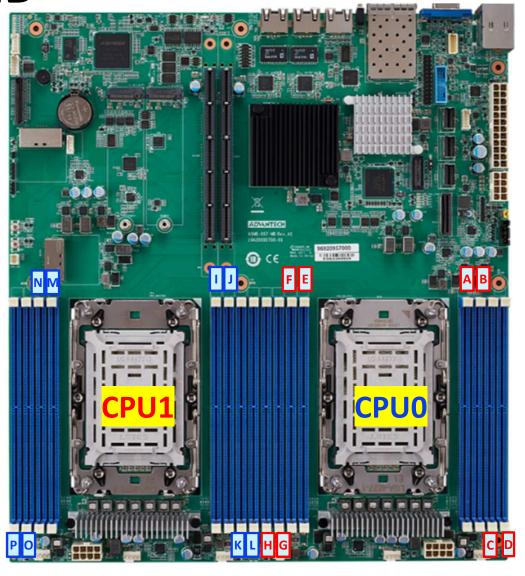
## CPU / M.2 / Riser slot location



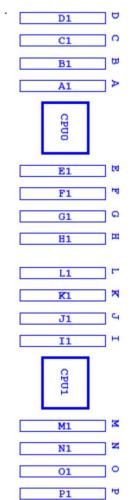
Multi-socket Intel systems shall be populated with identical CPUs Intel® Processor Installation Support for Desktops and Servers



## **DIMM Slot ID**

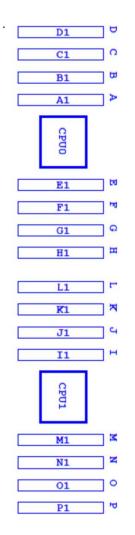


## **DIMM Population rules [1/2]**



		nfig																		
CPU0									CPU1											
Channe	l	Α	В	С	D	E	F	G	н	1	J	к	L	М	N	0	Р			
		٧								٧										
			V								V									
	2					V								٧						
DIMM							٧								٧					
Numbers		٧						V		V						٧				
	4			V		V						V		V						
	8	٧		V		V		V		٧		V		V		V				
		٧		V	٧	V	٧	V		V		V	٧	V	٧	V				
		٧	V	V		V		V	٧	V	٧	V		V		V	٧			
	12		V	V	٧	V	٧		٧		V	V	٧	V	V		V			
		٧	V		V		٧	V	٧	٧	٧		٧		٧	V	٧			
	16	٧	V	V	V	V	٧	V	V	V	V	V	٧	V	٧	V	V			

# DIMM Population rules [2/2]



IMC#		iM	IC3		iMC2						iM	CO		iMC1			
	Chan 1 (7/H)		Chan 0 (6/G)		Chan 1 (5/F)		Chan 0 (4/E)			Chan 0 (0/A)		Chan 1 (1/B)		Chan 0 (2/C)		Chan 1	1 (3/D)
DDRS	Slot0	Slot1	Slot0	Slot1	Slot0	Slot1	Slot0	Slot1		Slot1	Slat0	Slot1	Slat0	Slot1	Slot0	Slot1	Slot0
											DDRS						
1							DDRS										
1													DDRS				
					DDB5												
2			DORS								DDRS						
~							DDBS		C						DOBS		
4			DDB5				DORS				DORS				DORS		
			DDR5		DORS		DDFIS		P		DDRS				DDRS		DDRS
6	DORS		DORS				DORS				DORS		DORS		DDRS		
9	DORS				DDRS		DORES		U				DDRS		DDRS		DORS
	DDRS		DDR5		DDRS				0		DDRS		DDRS				DDRS
8	DORS		DDRS	أستسا	DDRS		DDRS				DDRS		DDRS		DDRS		DDRS
12	DORS		DDR5	DDBS	DDRS		DDRS	DDRS		DDRS	DDRS		DDRS	DDRS	DDRs		DDRS
	DORS	DORS	DDRS		DDRS	DDRS	DORS				DDRS	DORS	DORS		DDRS	DORS	DDRS
16	DORS	DDRS	DDRS	DDRS	DDRS	DDRS	DDRS	DDRs		DDRS	DDRS	DDRS	DORS	DDRS	DDRS	DDRS	DDRS

IMC#		iM	IC3		iMC2						iM	co	iMC1				
	Chan 1	L (7/P)	Chan (	(6/ <b>O</b> )	Chan	1 (5/N	Chan 6	0 (4/ <b>N</b> 1		Chan (	(0/1)	Chan 1	(1/1)	Chan (	(2/K)	Chan 1	L (3/L
DDRS	Slot0	Slot1	Slot0	Slot1	Slot0	Slot1	Slot0	Slot1		Slot1	Slot0	Slot1	Siot0	Slot1	Slot0	Slot1	Slot
											DDRS						
1							DDRS										
-													DDRS				
					DDRS												
2			DORS								DDRS						_
							DDRS		C						DORS		
4			DORS				DDRS		_		DDRS				DORS		
			DOR5		DDRS		DORS		P		DDRS				DORS		DO
6	DORS		DOR5				DORES				DDRS		DDR5		DDRS		
	DORS				DDRS		DDRS		U				DDRS		DORS		DD
	DORS		DDR5		DDRS				1		DDRS		DORS				DD
8	DORS		DDRS		DDRS		DDRS		-		DDRS		DORS		DORS		DD
12	DDRS		DDRS	DDRS	DDRS		DDRS	DORS		DDRS	DDRS		DDRS	DDRIS	DORS		DO
	DORS	DDRS	DDRS		DDRS	DDRS	DDRS				DORS	DDRIS	DORS		DORS	DDRS	DD
16	DORS	DDRS	DDRS	DORS	DDRS	DORS	DORS	DDRS		DDRS	DDRS	DDRS	DDRS	DDRS	DDRS	DDRS	DD

## Access the BIOS via Monitor with VGA port [1/2]

There are 3 ways to access platform for OS image installation

- > VGA port for OS in TXT & GUI mode
- Console port for OS in TXT only
- > BMC Web for OS in TXT & GUI mode Following pages will introduce page by page.



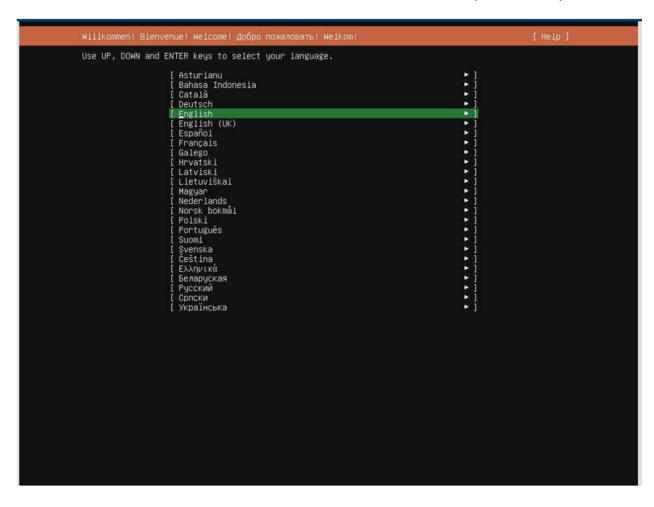
Picture depicted the connector of VGA D-Sub 9. VGA output in Front Panel

- Step#1 Power on the device
  - Prerequisite:
    - ✓ Get AC 100-240V @ 50-60Hz, full range
  - Device will boot:
    - ✓ Correct behavior: you can hear FAN rotating in maximum speed for a while then down and also the Power LEDs should light up



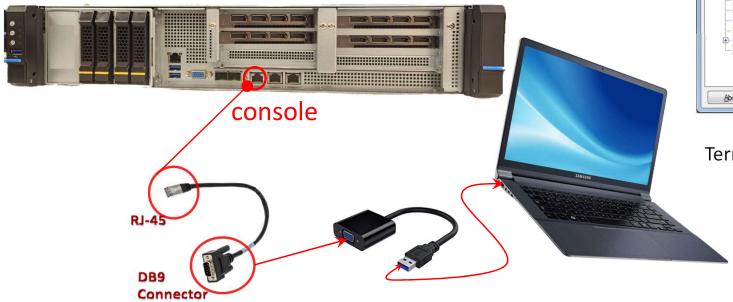
## Operation System installation from VGA Port [2/2]

Connect D-Sub 9 via VGA cable to Monitor and USB port to keyboard and mouse

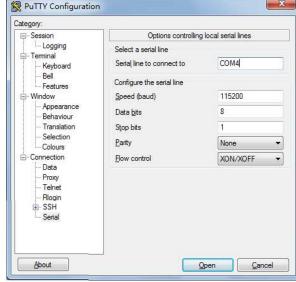


## Access the server via Console port [1/2]

- ☐ Step#2 Access the device
  - o Prerequisite:
    - ✓ Console cable and PC + Terminal(like PuTTY)
  - Connect the PC to the server console



#### Terminal settings



#### **Terminal Setting:**

Baud rate: 115200

■ Data bits : 8

Stop bits: 1



## Access the server via Console port [2/2]

```
UEFI Interactive Shell v2.2

EDK II

UEFI v2.80 (American Megatrends, 0x00050016)

Mapping table

BLK0: Alias(s):

PciRoot(0x0)/Pci(0x11,0x5)/Sata(0x0,0xFFFF,0x0)

BLK1: Alias(s):

PciRoot(0x0)/Pci(0x17,0x0)/Sata(0x0,0xFFFF,0x0)

BLK2: Alias(s):

PciRoot(0x0)/Pci(0x17,0x0)/Sata(0x4,0xFFFF,0x0)

Press ESC in 1 seconds to skip startup.nsh or any other key to continue.

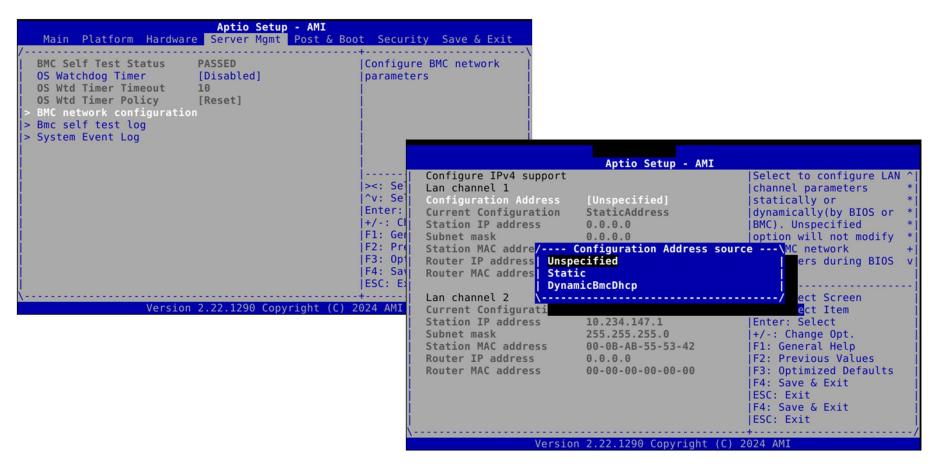
Shell>
```

Picture depicted successfully server access via console if no OS installed

## BMC IP address configure from BIOS [1/2]

Setup BMC IP Address

Press **DEL** after boot up to enter BIOS, and select "Server Mgmt" page, then "BMC network configuration".





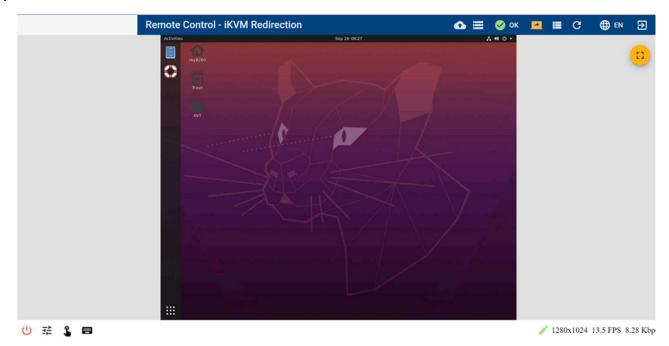
## BMC access via browser [2/2]

After completing the steps above (BMC Web ip configured via either Bios or iptool), open your favorite browser the enter the webui ip as below: <a href="https://bmcip">https://bmcip</a>

The default login credentials by default:

• User: administrator

• Password: advantech





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